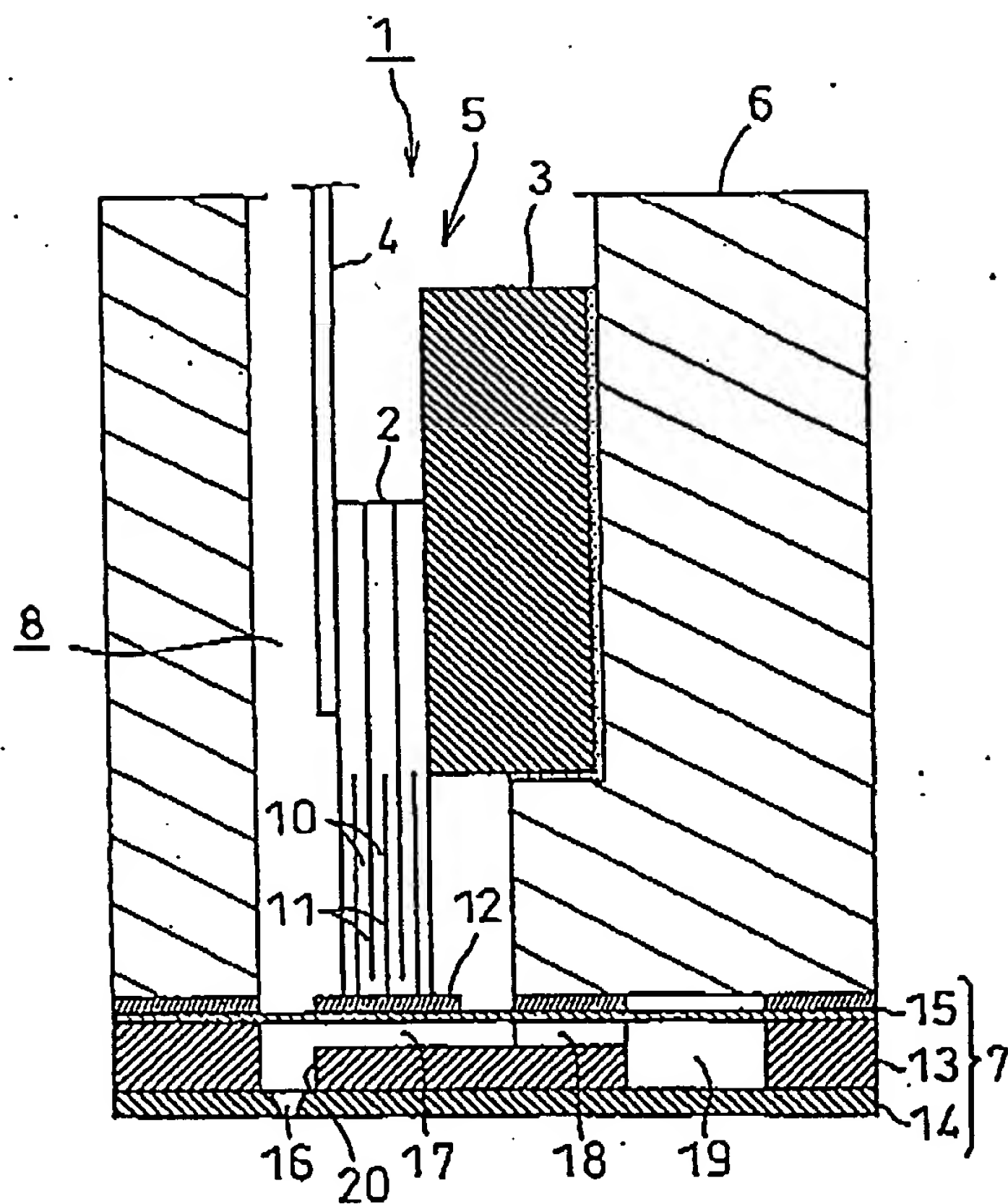


Fig. 1



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Fig. 4

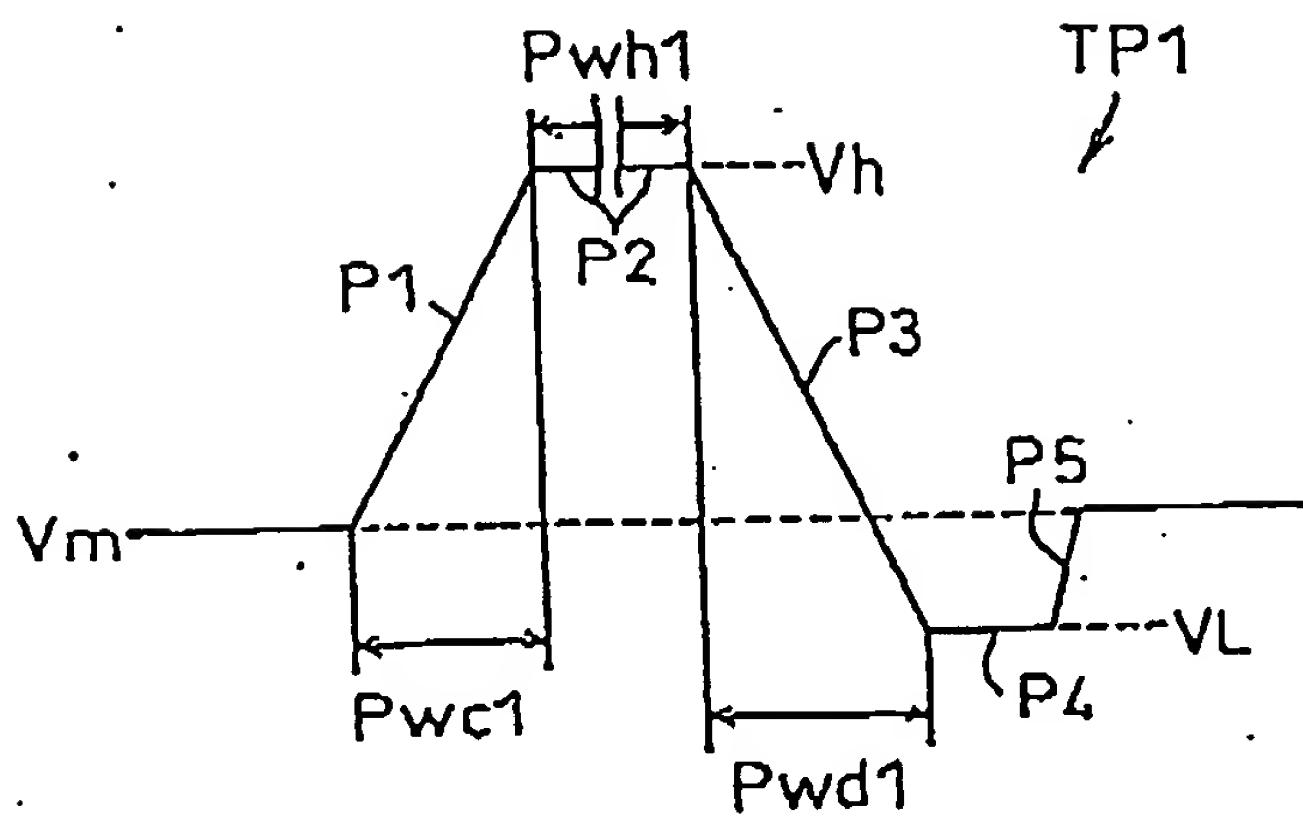


Fig. 5

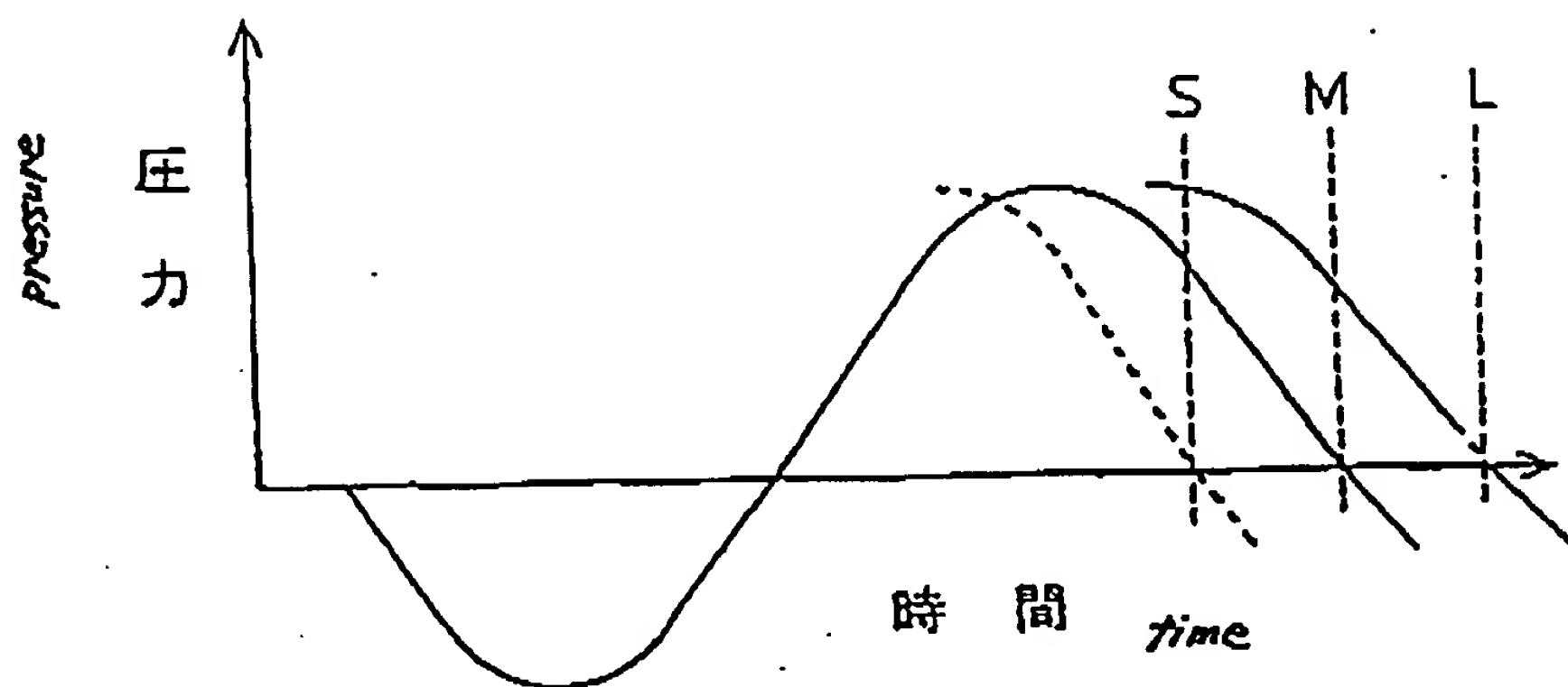
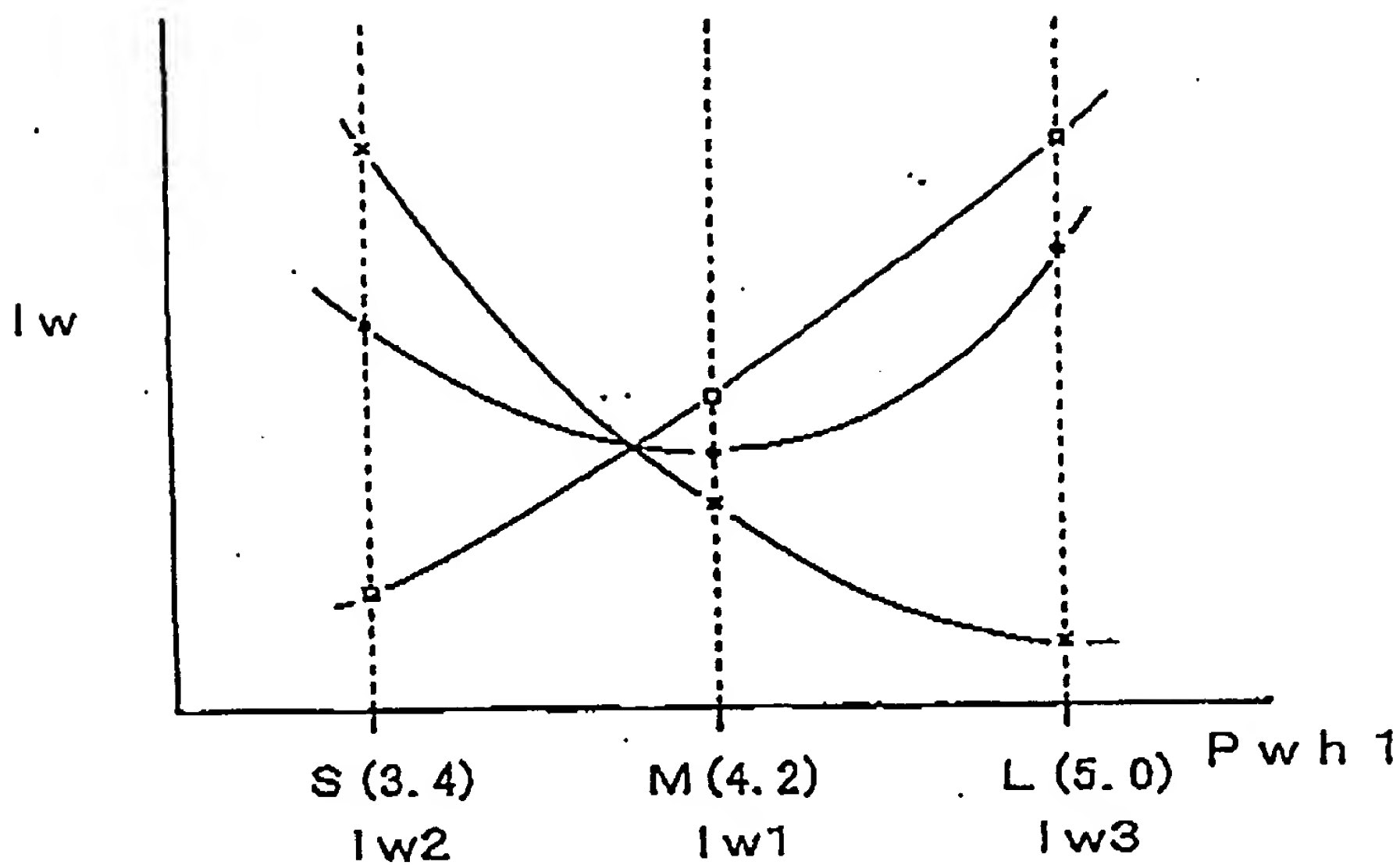


Fig. 6

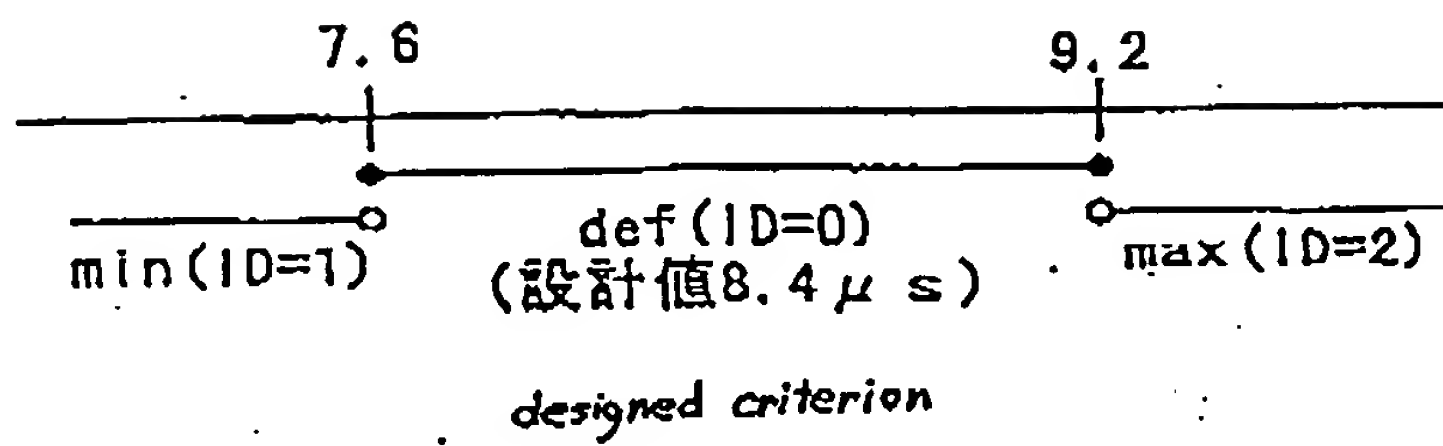


		$I w$			
Tc ランク ID	0	$I w2 > I w1 < I w3$			↘
		$I w2 > I w1 = I w3$			↘↘
		$I w2 = I w1 < I w3$			↘↘↘
	1	$I w2 < I w1 < I w3$			↗
	2	$I w2 > I w1 > I w3$			↗↗
	エラー	$I w2 = I w1 = I w3$			↔↔↔
		$I w2 = I w1 > I w3$			↘↘↔
		$I w2 < I w1 = I w3$			↗↗↔
		$I w2 < I w1 > I w3$			↗↗↗

Fig. 7

Tc rank ID error

Fig. 8



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Fig. 9

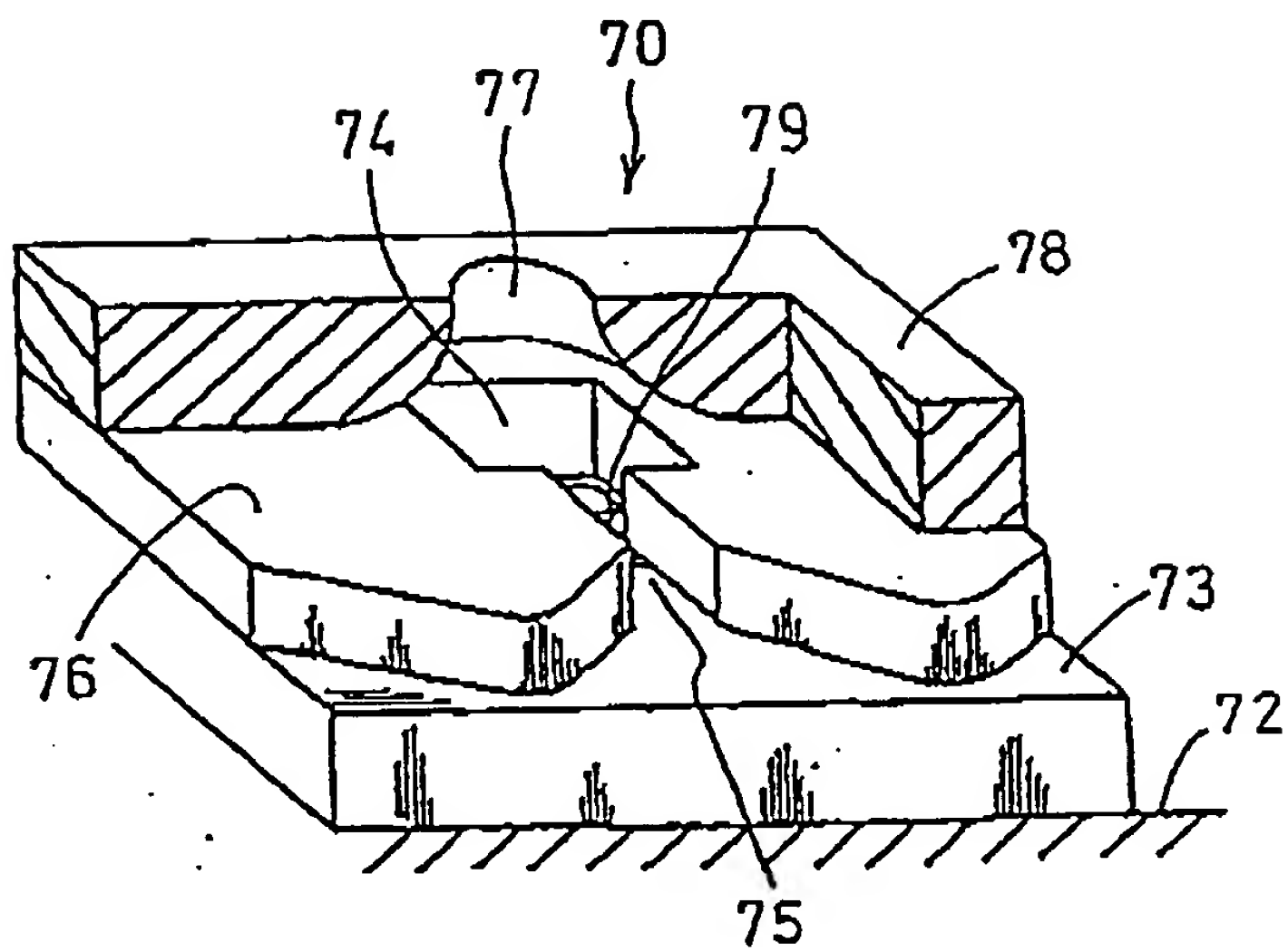


Fig. 10

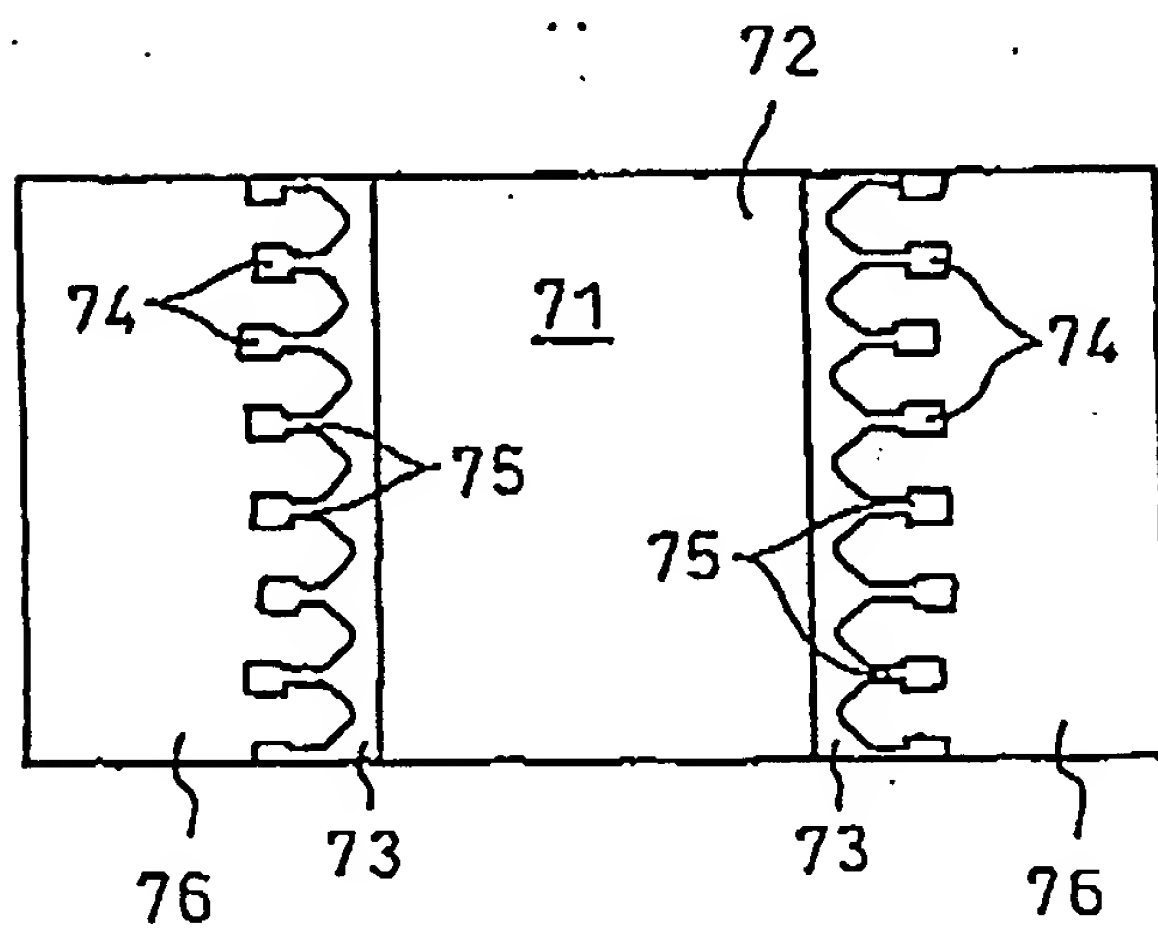


FIG. 9

Fig. 11

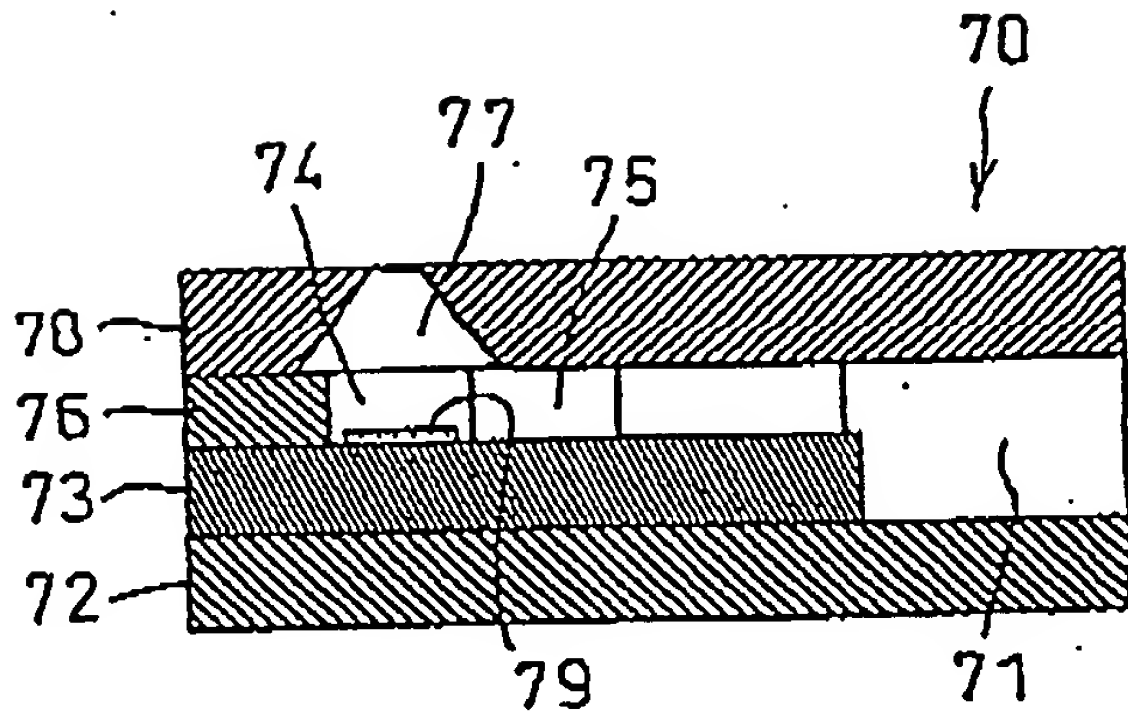


Fig. 12A

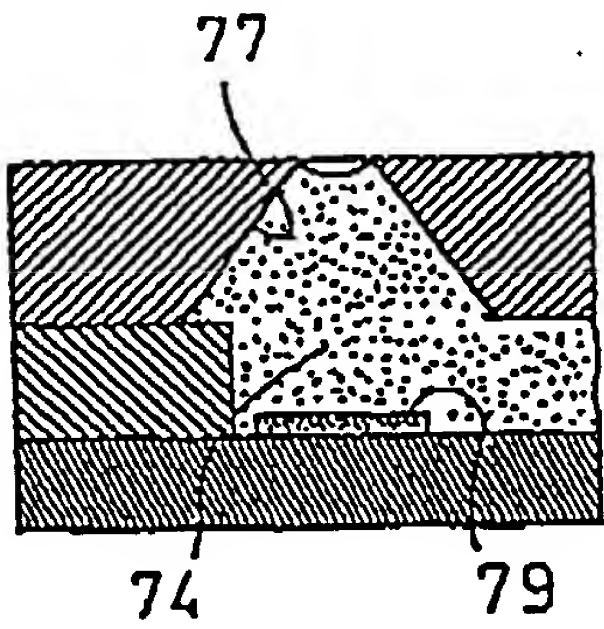


Fig. 12B

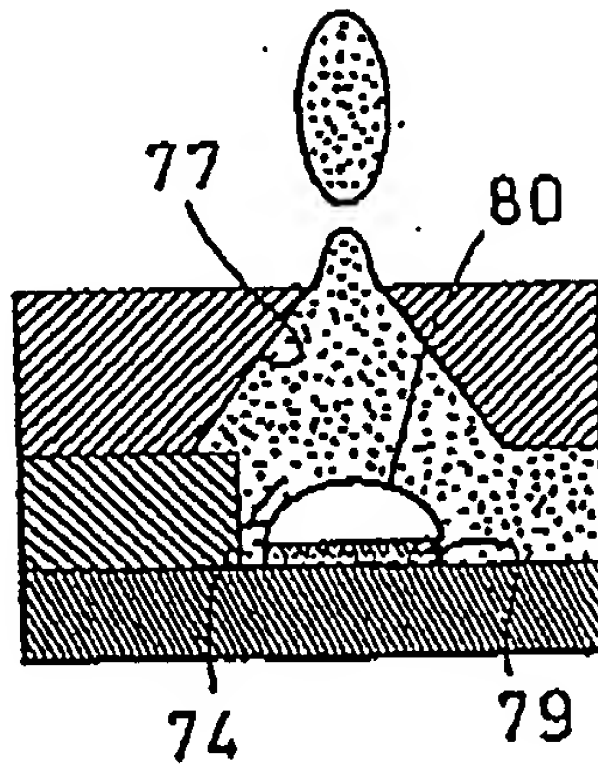
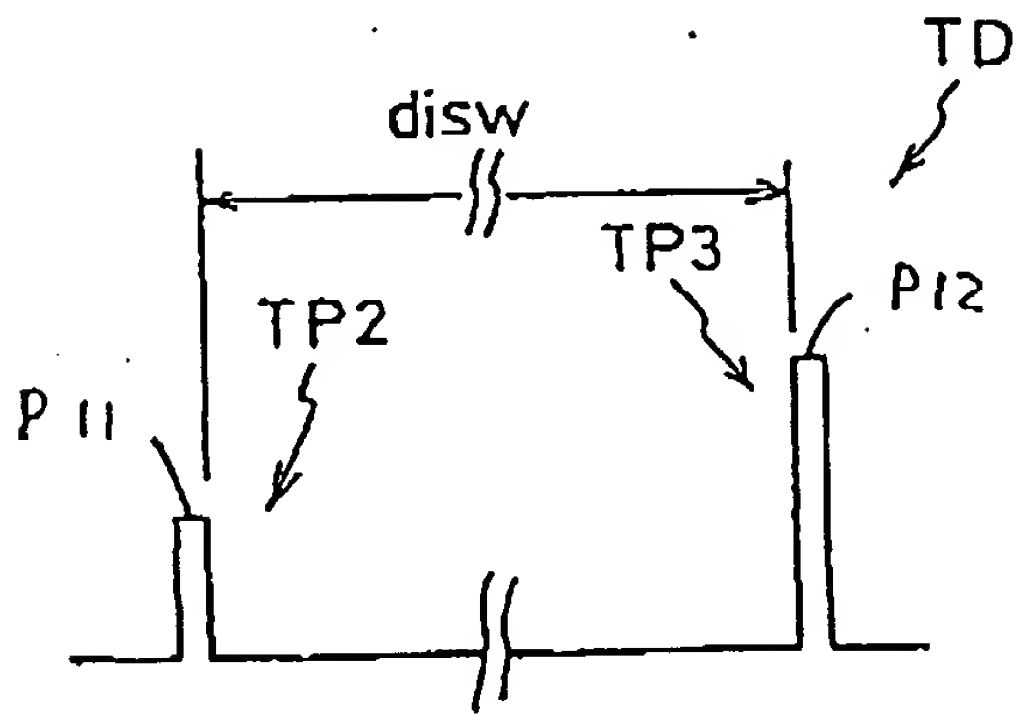


FIG. 11

Fig. 13



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Fig. 14

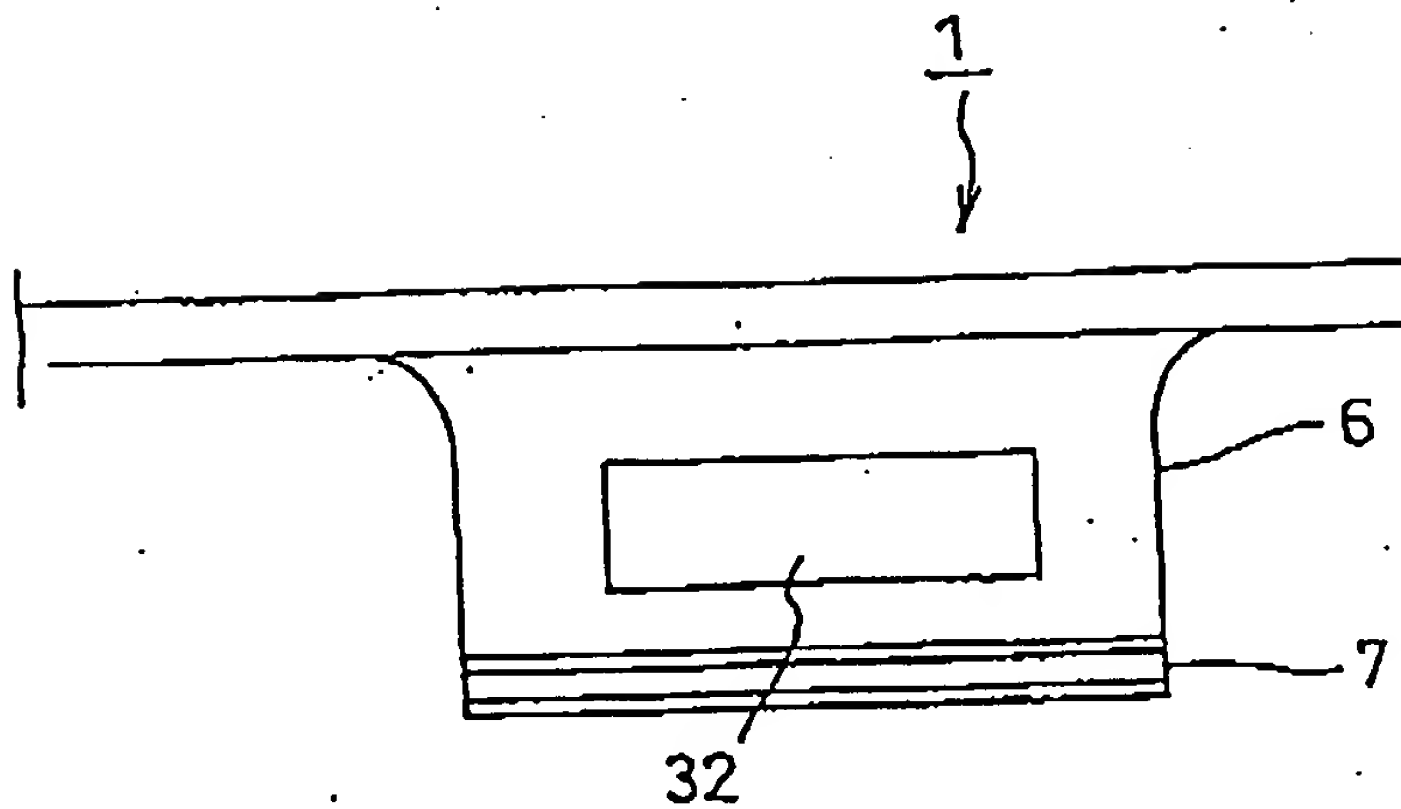
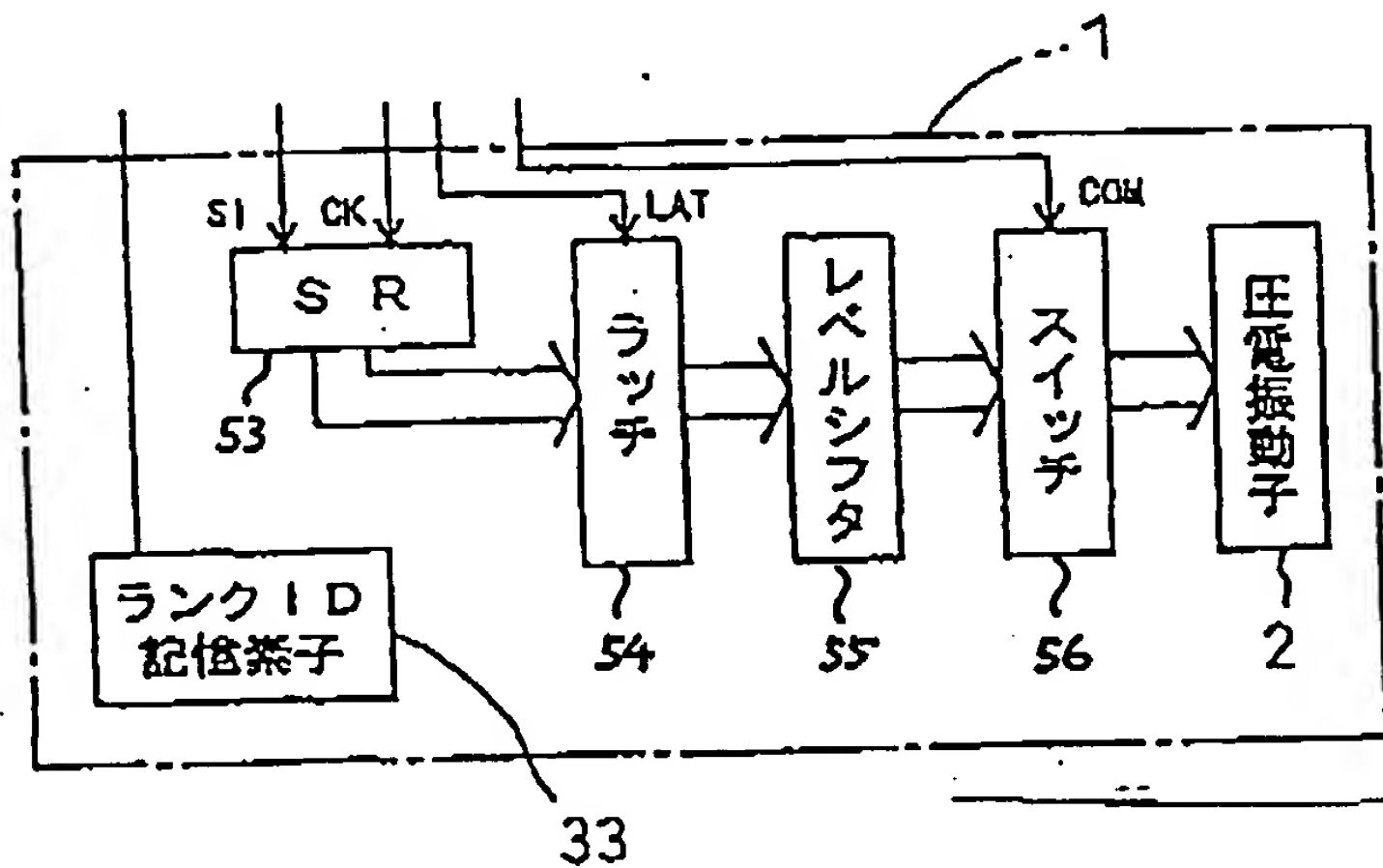
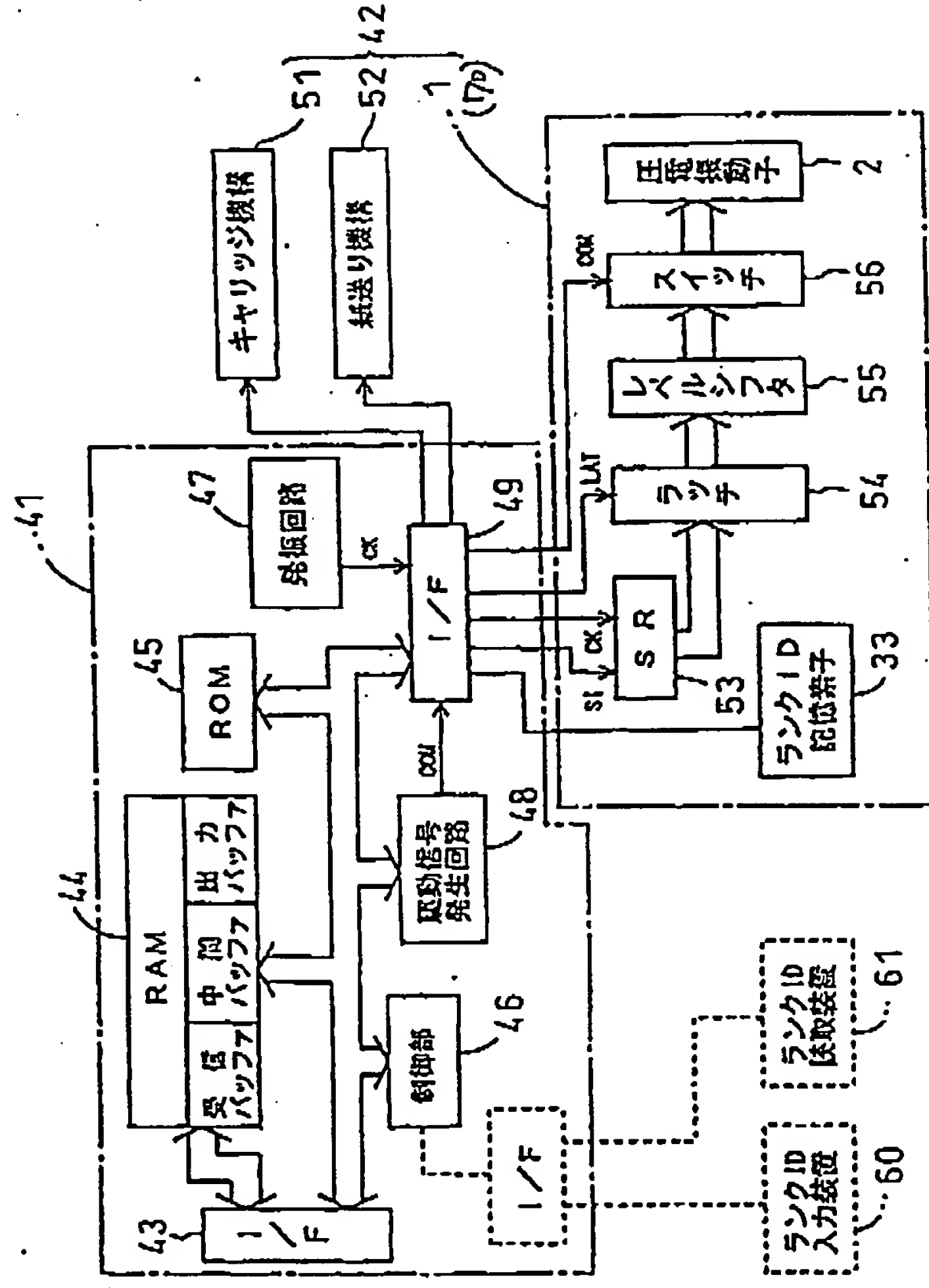


Fig. 15



- 2: piezoelectric vibrator
- 33: rank memory
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher

Fig. 16



- 2: piezoelectric vibrator
- 33: rank ID memory
- 44: reception buffer
- 44: intermediate buffer
- 44: output buffer
- 46: controller
- 47: oscillator
- 48: drive signal generator
- 51: carriage feeding mechanism
- 52: paper feeding mechanism
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher
- 60: rank ID input device
- 61: rank ID reader

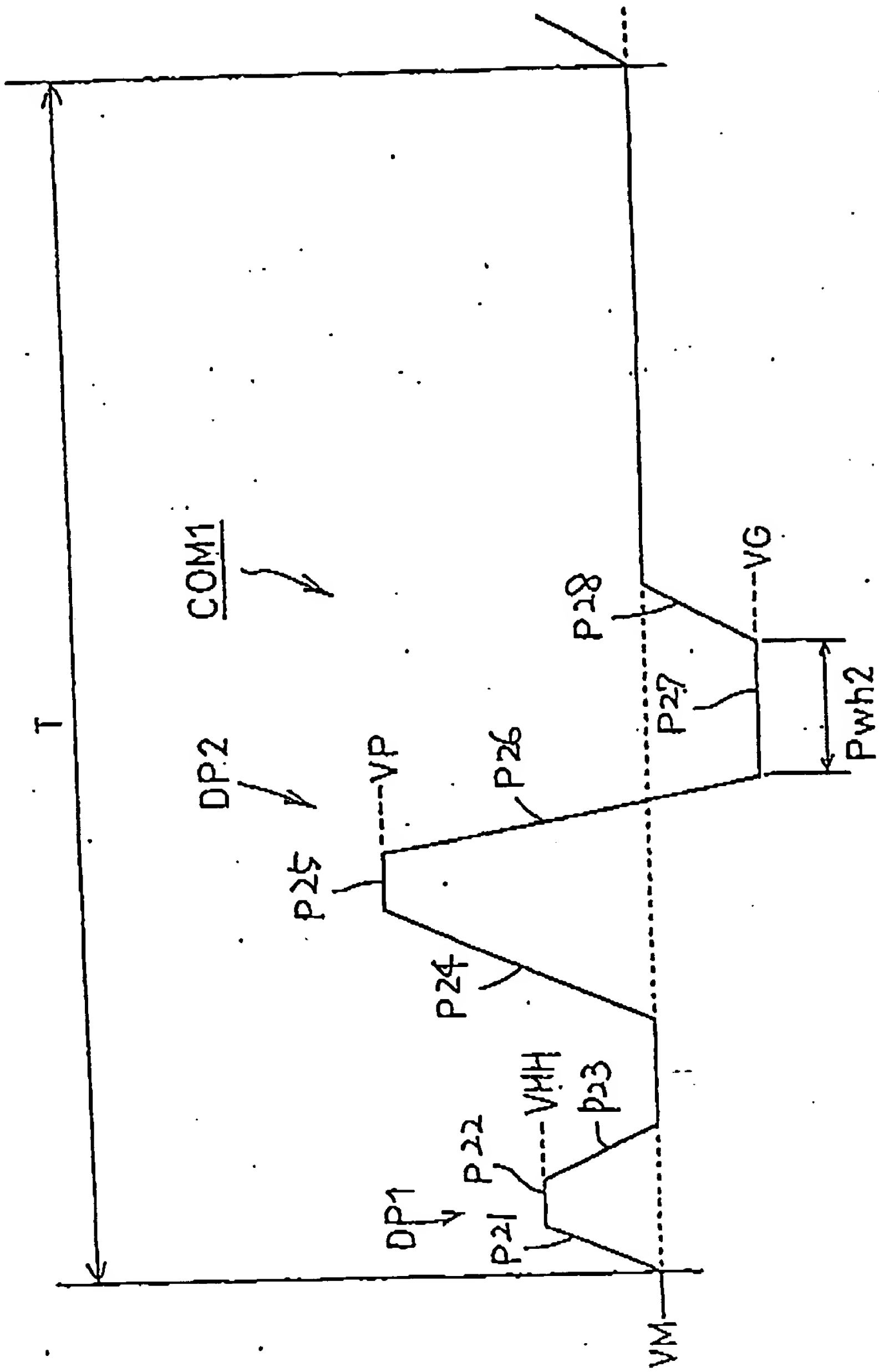
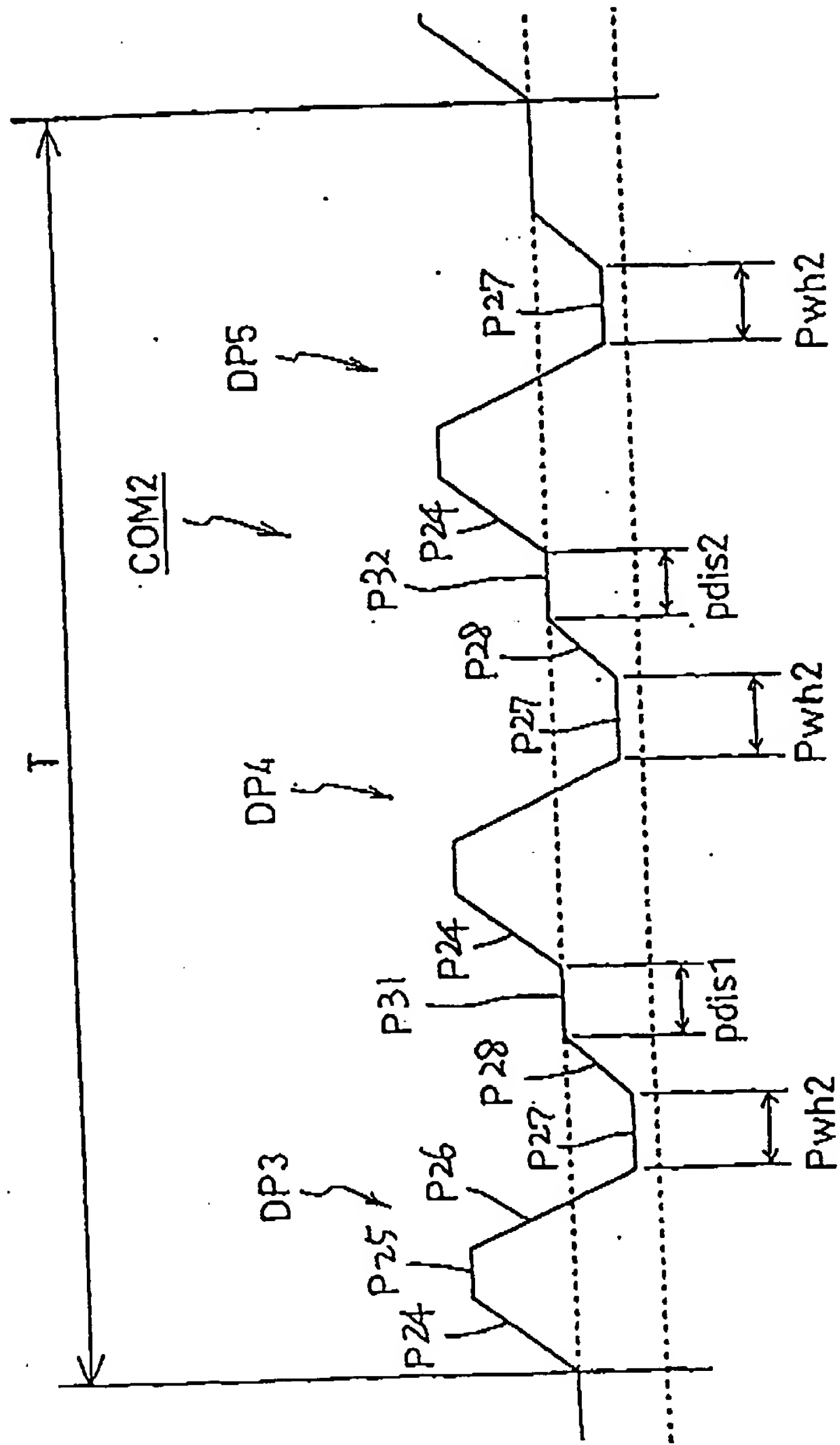


FIG. 18



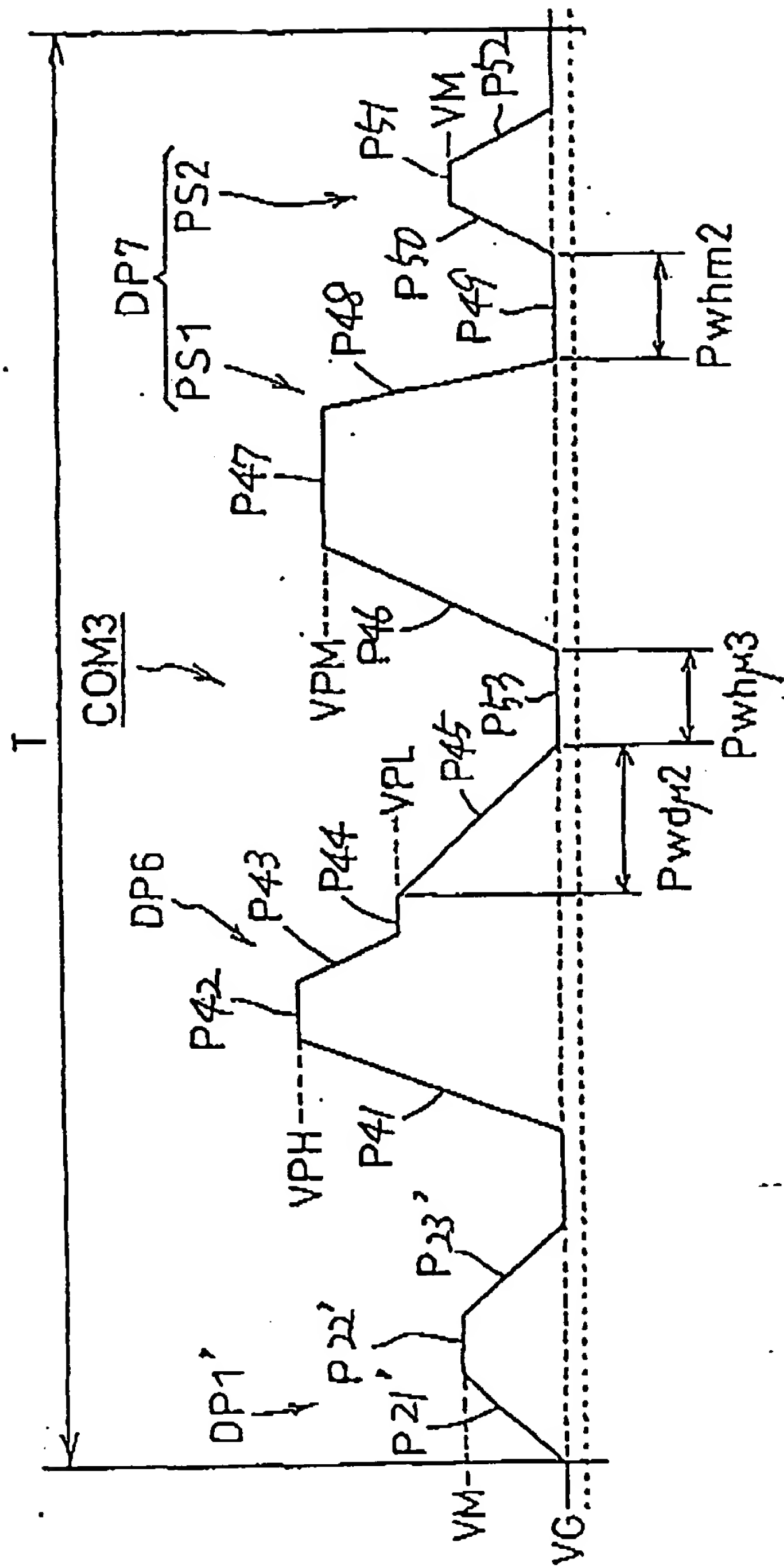


Fig. 20

COM4

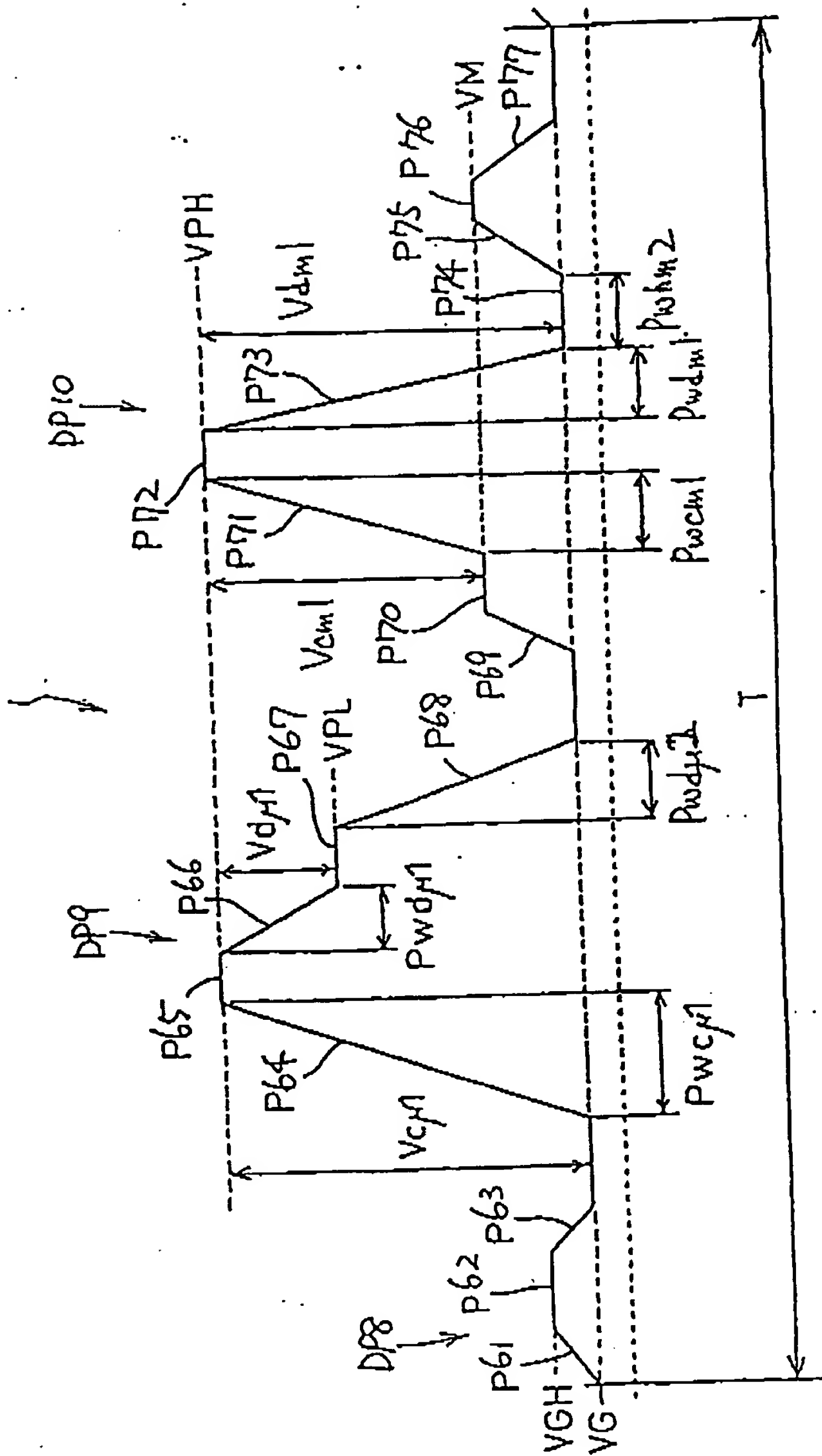
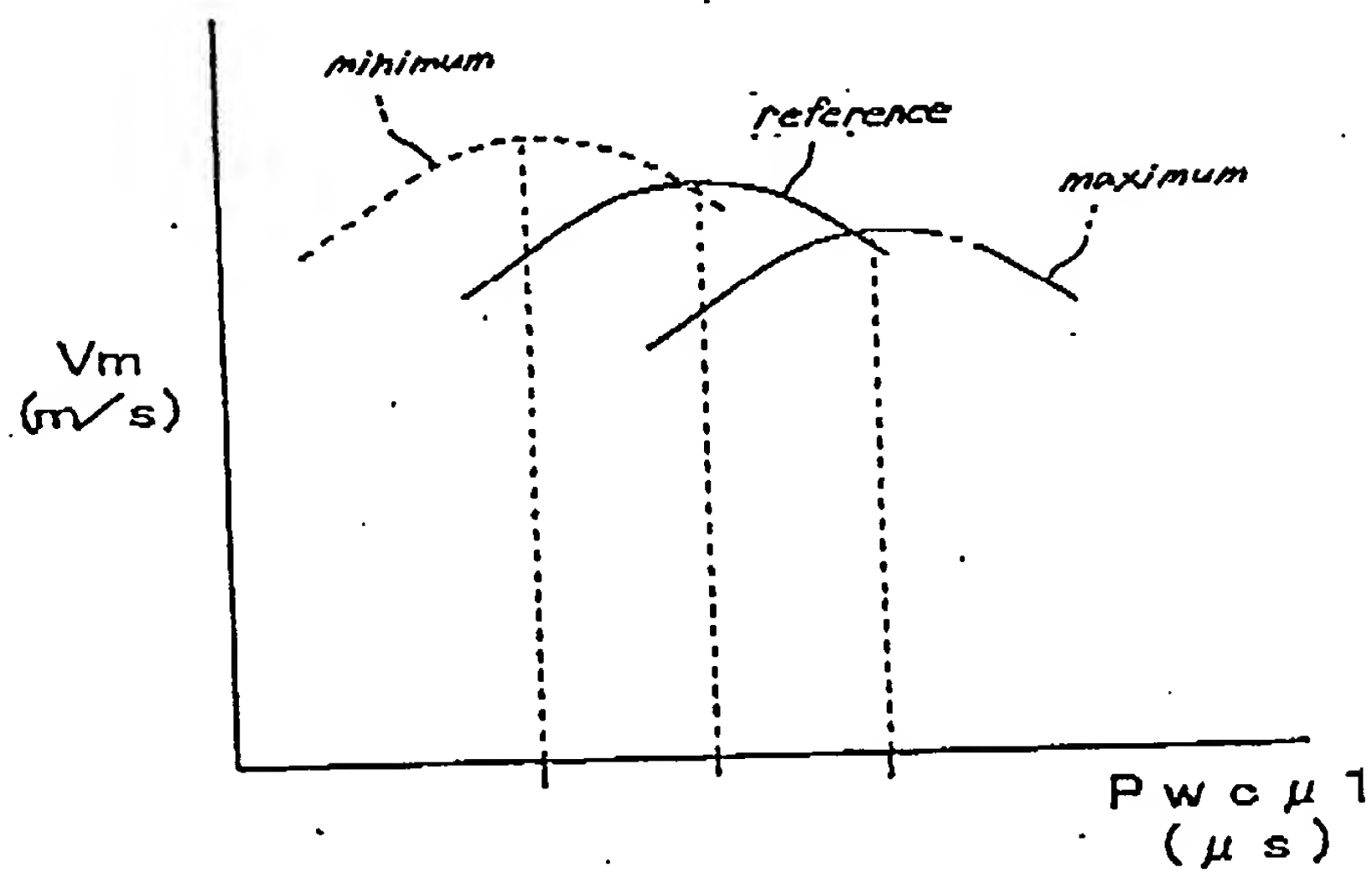


Fig. 21



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Fig. 22

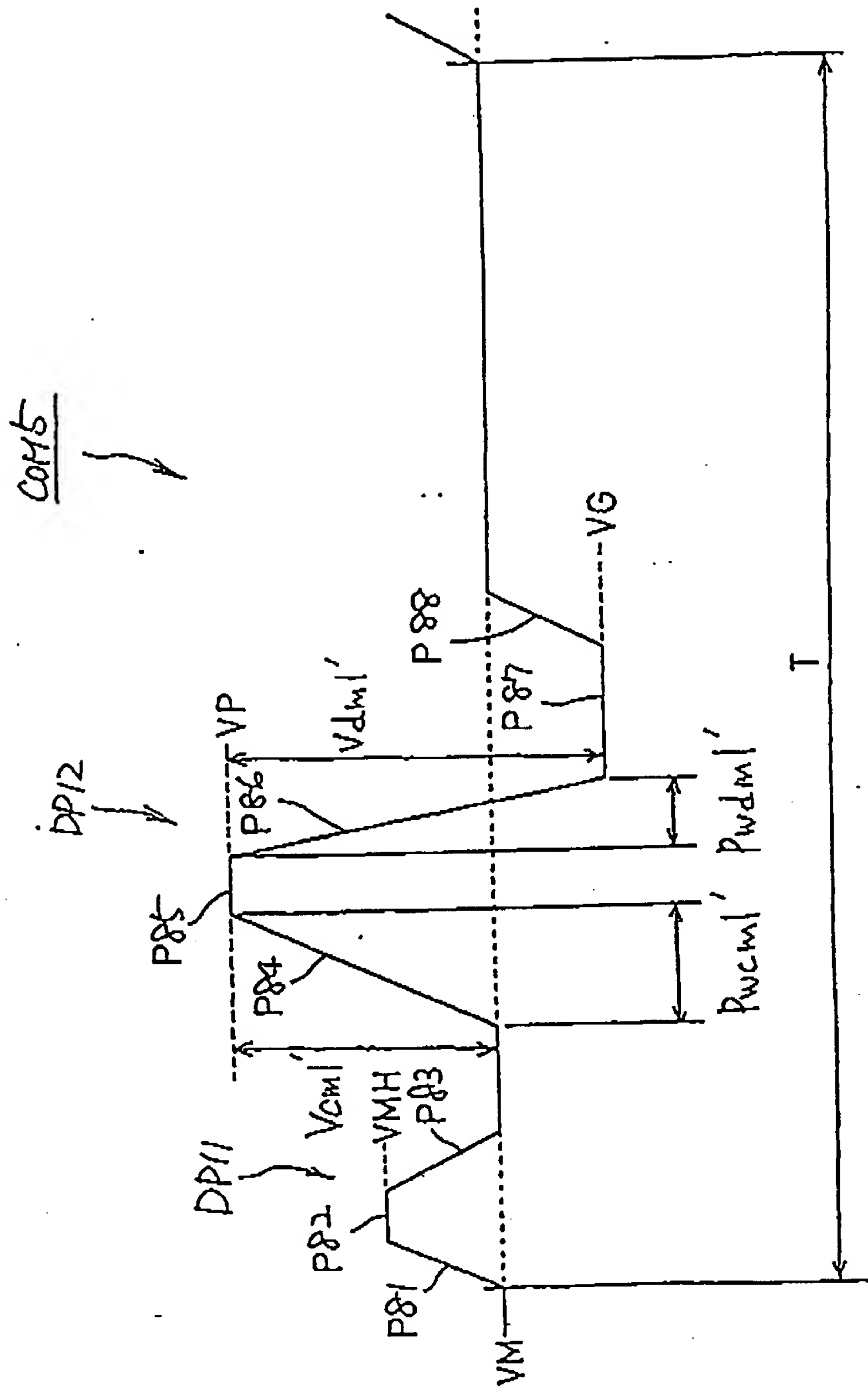


Fig. 23

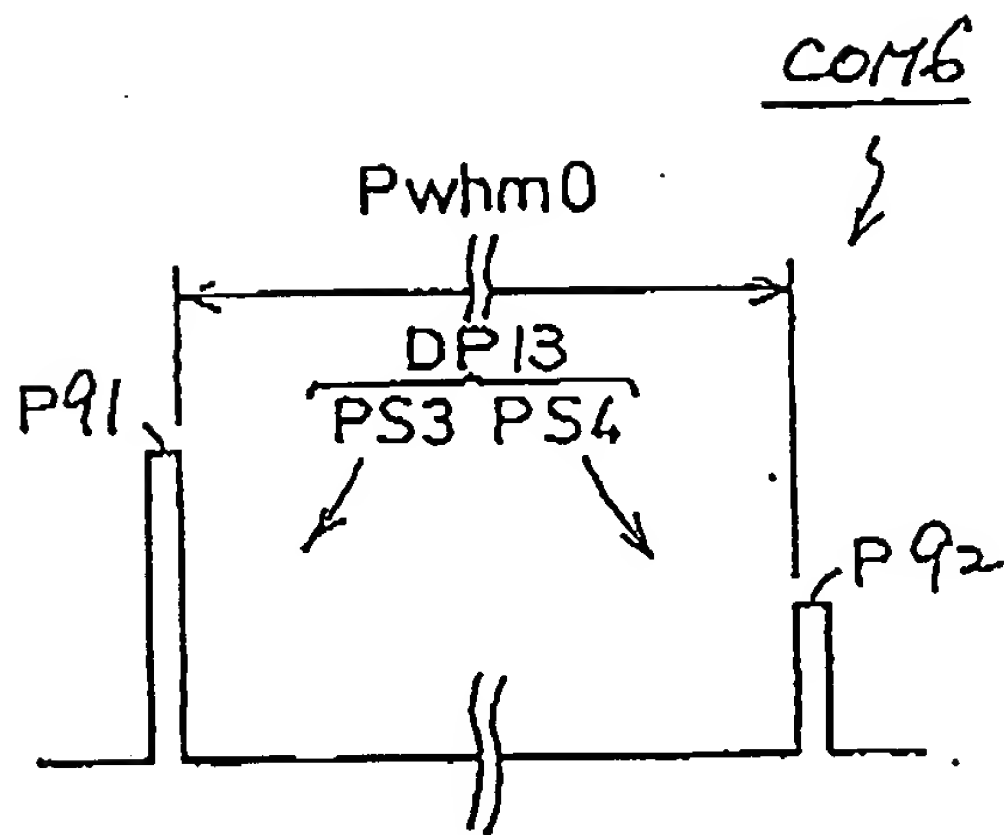
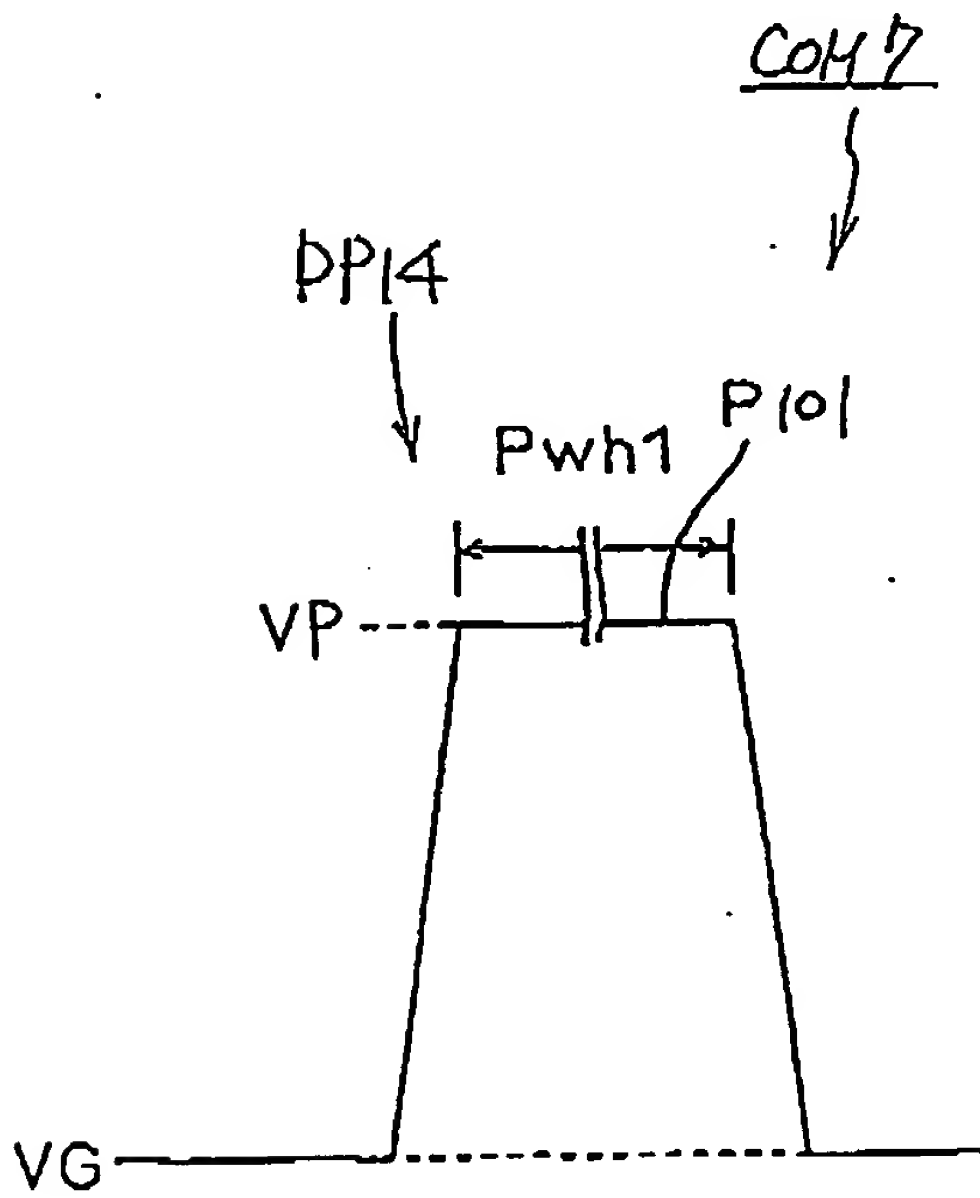


Fig. 24



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